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From: Andrew C. Graham	Senders Phone No.: (512) 338-6300
	Senders Fax No.: (512) 338-6301

REGARDING:

Title:	MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND LOGIC BLOCK STRUCTURE AND METHOD THEREFOR		
Application No.: 10/675,212	Filed:	September 30, 2003	
Examiner: Ho, Hoai V.	Group Art Unit:	2818	
Atty. Docket No.: 023-0024	Confirmation No.:	9946	

ATTACHED HERETO:

- (1) Response to the Non Final Office Action dated October 13, 2004 (8 pages)

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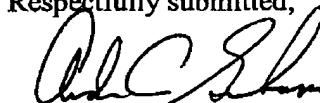
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Andrew C. Graham

Date

1-13-05

Respectfully submitted,



Andrew C. Graham, Reg. No. 36,531
Attorney for Applicant(s)
(512) 338-6313 (direct)
(512) 338-6301 (fax)

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PATENT

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Applicant(s): Roy E. Scheuerlein, et al.

Title: MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND
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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE TO NON-FINAL OFFICE ACTION**

This paper is responsive to the Non-Final Office Action mailed on October 13, 2004, having a shortened statutory period for response set to expire January 13, 2005. In light of the Amendments and/or Remarks herein, further examination is requested.